

PIN	PORT	FUNKTION	PIN	PORT	FUNKTION
0	E0	RXD protected	35	C2	X_ENABLE_PIN protected
1	E1	TXD protected	36	C1	LCD_PINS_D7 Output = 1
2	E4	<unused/unknown> Input = 0 TIMER3B PWM: 0 WGM: 1 COM3B: 0 CS: 3 TCCR3A: 1 TCCR3B: 3 TIMSK3: 0	37	C0	X_STEP_PIN protected
3	E5	HEATER_1_PIN Output = 0 TIMER3C PWM: 0 WGM: 1 COM3C: 0 CS: 3 TCCR3A: 1 TCCR3B: 3 TIMSK3: 0	38	D7	SD_DETECT_PIN Input = 1
4	G5	HEATER_BED_PIN protected	39	G2	X_DIR_PIN protected
5	E3	LCD_PINS_D6 Output = 1 TIMER3A PWM: 0 WGM: 1 COM3A: 0 CS: 3 TCCR3A: 1 TCCR3B: 3 TIMSK3: 0	40	G1	BTN_EN2 Input = 1
6	H3	CASE_LIGHT_PIN PWM: 105 TIMER4A PWM: 105 WGM: 1 COM4A: 2 CS: 3 TCCR4A: 129 TCCR4B: 3 TIMSK4: 0 . LED_PIN PWM: 105	41	G0	<unused/unknown> Input = 0
7	H4	<unused/unknown> Input = 0 TIMER4B PWM: 0 WGM: 1 COM4B: 0 CS: 3 TCCR4A: 129 TCCR4B: 3 TIMSK4: 0	42	L7	BTN_EN1 Input = 1
8	H5	<unused/unknown> Input = 0 TIMER4C PWM: 0 WGM: 1 COM4C: 0 CS: 3 TCCR4A: 129 TCCR4B: 3 TIMSK4: 0	43	L6	<unused/unknown> Input = 0
9	H6	FAN_PIN protected	44	L5	E0_DIR_PIN protected
10	B4	HEATER_0_PIN protected	45	L4	<unused/unknown> Input = 0 TIMER5B PWM: 0 WGM: 1 COM5B: 0 CS: 3 TCCR5A: 1 TCCR5B: 3 TIMSK5: 0
11	B5	SERVOO_PIN Input = 0 TIMER1A PWM: 2000 WGM: 4 COM1A: 0 CS: 2 TCCR1A: 0 TCCR1B: 10 TIMSK1: 2 non-s	46	L3	E0_STEP_PIN protected
12	B6	E0_ENABLE_PIN protected . PS_ON_PIN protected	47	L2	E1_DIR_PIN protected
13	B7	<unused/unknown> Input = 0 TIMER0A PWM: 0 WGM: 3 COM0A: 0 CS: 3 TCCR0A: 3 TCCR0B: 3 TIMSK0: 5 overflow interrupt enabled . TIMER1C is also tied to this pin TIMER1C PWM: 0 WGM: 4 COM1C: 0 CS: 2 TCCR1A: 0 TCCR1B: 10 TIMSK1: 2 non-standard PWM mode	48	L1	E1_ENABLE_PIN protected
14	J1	<unused/unknown> Input = 0	49	L0	E1_STEP_PIN protected
15	J0	<unused/unknown> Input = 0	50	B3	AVR_MISO_PIN Input = 0 . MISO_PIN Input = 0
16	H1	LCD_PINS_D4 Output = 0	51	B2	AVR_MOSI_PIN Output = 1 . MOSI_PIN Output = 1
17	H0	LCD_PINS_ENABLE Output = 0	52	B1	AVR_SCK_PIN Output = 0 . SCK_PIN Output = 0
18	D3	BEEPER_PIN Output = 0	53	B0	AVR_SS_PIN Output = 1 . SDSS Output = 1 . SS_PIN Output = 1
19	D2	BTN_ENC Input = 1	54	F0 (A 0)	SUICIDE_PIN Output = 1
20	D1	LCD_PINS_RS Output = 1 . SDA Output = 1	55	F1 (A 1)	<unused/unknown> Analog in = 683 Input = 1
21	D0	LCD_PINS_D5 Output = 1 . SCL Output = 1	56	F2 (A 2)	<unused/unknown> Analog in = 811 Input = 1
22	A0	<unused/unknown> Input = 0	57	F3 (A 3)	<unused/unknown> Analog in = 782 Input = 1
23	A1	Z_DIR_PIN protected	58	F4 (A 4)	<unused/unknown> Analog in = 756 Input = 1
24	A2	X_MIN_PIN protected	59	F5 (A 5)	<unused/unknown> Analog in = 765 Input = 1
25	A3	Z_STEP_PIN protected	60	F6 (A 6)	<unused/unknown> Analog in = 627 Input = 1
26	A4	<unused/unknown> Input = 0	61	F7 (A 7)	<unused/unknown> Analog in = 781 Input = 1
27	A5	Z_ENABLE_PIN protected	62	K0 (A 8)	<unused/unknown> Analog in = 1023 Input = 1
28	A6	Y_MIN_PIN protected	63	K1 (A 9)	TEMP_1_PIN Analog in = 1023
29	A7	Y_ENABLE_PIN protected	64	K2 (A10)	TEMP_BED_PIN protected
30	C7	Z_MIN_PIN protected	65	K3 (A11)	TEMP_0_PIN protected
31	C6	Y_STEP_PIN protected	66	K4 (A12)	FIL_RUNOUT_PIN Input = 1
32	C5	<unused/unknown> Input = 0	67	K5 (A13)	<unused/unknown> Analog in = 1023 Input = 1
33	C4	Y_DIR_PIN protected	68	K6 (A14)	<unused/unknown> Analog in = 875 Input = 1
34	C3	<unused/unknown> Input = 0	69	K7 (A15)	<unused/unknown> Analog in = 425 Input = 0